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AMENDMENTS TO THE SPECIFICATION

Please amend the first paragraph on Page 16 of the Specification as follows:

With respect to either the open loop or the closed loop process, the synchronization circuit 61 receives the signals that had been converted into discrete time samples by the ADC of the OFDM demodulator 22 illustrated in Fig. 5. In the synchronization circuit 61, the signals enter a time synchronization process 84. An exemplary time synchronization process has been described in the commonly owned, pending patent application entitled: Time and Frequency Synchronization in Multi-Input, Multi-Output (MIMO) Systems, which was filed in the United States Patent and Trademark Office on April 24, 2002 and assigned Serial No. 10/128,821, now U.S. Patent No. 7,088,782, and which is incorporated herein by reference.

Please amend the final paragraph on Page 21 of the Specification as follows:

Finally, the error is separated into its integer ~~(integer(err))~~ (integer(err)) and fractional (fractional(err)) components and sent to the timing synchronization and phase rotation circuits, respectively.